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12/02/2003

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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
1,0/080,586	02/25/2002	Yasutoshi Aibara	XA-9625	8708

TITLE OF INVENTION: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND IMAGING SYSTEM

	SMALL ENTITY	ISSUE FI	EE	PUBLICATION FEE	TOTAL F	EE(S) DUE	DATE DUE
nonprovisional	NO	\$1330	)	\$300	\$1	630	03/02/2004
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ALLEN, S'	ГЕРНОМЕ В	2878		250-21400A	•		
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Renesas Technology Corp.

Tokyo, Japan

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Tokyo, Japan

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